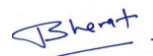


Dept. of ECE, MNIT Jaipur Time Table Odd Semester 2021-22
VII Semester B. Tech. (ECE)

| | 8-9 AM | 9-10 AM | 10-11 AM | 11-12 PM | 12-1 PM | 1-2 PM | 2-3 PM | 3-4 PM | 4-5 PM |
|-----------|--------|---|-------------------|-------------------|-------------------|--------|-------------------|---------------|--------|
| Monday | | UG Sys. Des. Lab – I; EC1; VLSI Lab; BC | | | ECT663 | Lunch | ECT413 / ECT670 | | |
| | | UG Sys. Des. Lab – I; EC2; EMBD Lab; D.Bharti | | | AEC | | CN / SC&R | | |
| | | Trg. Seminar; EC3; Opt. Comm. Lab; GS, K.S | | | RKM | | RB / IS | Open Elective | |
| | | Trg. Seminar; EC4; Comm Room; AMJ, SJN | | | VLTC L104 | | LT-15 / VLTC L104 | | |
| | | | ECT663 | ECT462 / ECT620 | ECT634 / ECT481 | | | | |
| Tuesday | | | AEC | AI&ES / MDC | MEMS / FPGA | | | | |
| | | | RKM | SJN / BC | CP / D.Bharti | | | Open Elective | |
| | | | VLTC L104 | VLTC L106 / LT-15 | VLTC L104 / L106 | | | | |
| Wednesday | | | ECT413 / ECT670 | | ECT462 / ECT620 | | ECT634 / ECT481 | | |
| | | | CN / SC&R | | AI&ES / MDC | | MEMS / FPGA | | |
| | | | RB / IS | | SJN / BC | | CP / D.Bharti | Open Elective | |
| | | | LT-15 / VLTC L104 | | VLTC L106 / LT-15 | | VLTC L104 / L106 | | |
| Thursday | | UG Sys. Des. Lab – I; EC3; VLSI Lab; MY | | | ECT663 | | | | |
| | | UG Sys. Des. Lab – I; EC4; EMBD Lab; D.Bharti | | | AEC | | | | |
| | | Trg. Seminar; EC1; Comm Room; R.S, S.S | | | RKM | | | Open Elective | |
| | | Trg. Seminar; EC2; Opt. Comm. Lab; K.S, IS | | | VLTC L104 | | | | |
| Friday | | | | ECT634 / ECT481 | ECT413 / ECT670 | | ECT462 / ECT620 | | |
| | | | | MEMS / FPGA | CN / SC&R | | AI&ES / MDC | | |
| | | | | CP / D.Bharti | RB / IS | | SJN / BC | Open Elective | |
| | | | | VLTC L104 / L106 | LT-15 / VLTC L104 | | VLTC L106 / LT-15 | | |

All free slots may be used for Final Year Project (ECD498) work.



(Dr. Bharat Choudhary)



(Dr. Rajesh Saha)



(Prof. Vijay Janyani)